

# Marvell Prestera 98CX8297

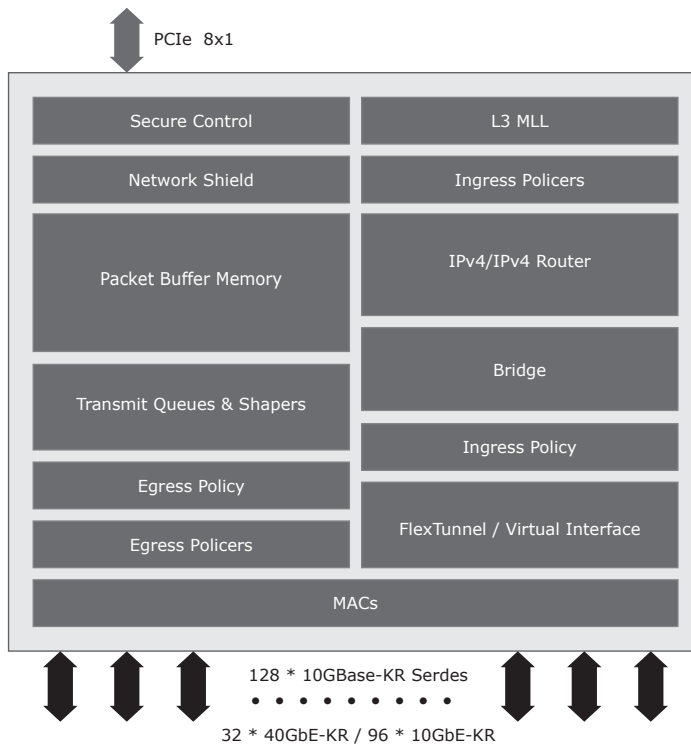
Next Generation Switch Fabric for Modular Platforms



## ▶ PRODUCT OVERVIEW

The Marvell® Prestera® CX family of packet processors enable high density 10GbE/40GbE solutions Datacenter Top of Rack (ToR), and Aggregation switches as well as Ethernet aggregation platforms for the Campus and Service Provider markets. The Prestera 98CX8297 provides line rate performance for up to 32 ports of 40 GbE or 96 ports of 10GbE with a full complement of Layer2/Layer3 switching, IPv4/v6 Unicast and Multicast routing capabilities along advanced classification and congesting management features.

## ▶ BLOCK DIAGRAM



## ▶ KEY FEATURES AND BENEFITS

SPECIAL FEATURES	BENEFITS
<ul style="list-style-type: none"> <li>• Integrated 10GBase-KR PHYs</li> <li>• Datacenter Bridging</li> </ul>	<ul style="list-style-type: none"> <li>• Drive backplanes for modular platforms in ATCA, blade server applications</li> <li>• Prioritization and Congestion management capabilities for next generation datacenter solution                             <ul style="list-style-type: none"> <li>– Priority Flow Control, Congestion Notification enable lossless mode of operation as well as burst isolation</li> </ul> </li> </ul>
<ul style="list-style-type: none"> <li>• Policy and classification engine</li> <li>• CarrierSpan™</li> </ul>	<ul style="list-style-type: none"> <li>• Built-in intelligence for application-level control</li> <li>• Full complement of Metro Ethernet standards along with the latest service delivery techniques viz L2/L3 VPN, Q-in-Q, TR-101 etc                             <ul style="list-style-type: none"> <li>– Enables business and consumer networking services</li> </ul> </li> </ul>

## ▶ APPLICATIONS

The Prestera 98CX8297 is a 1.2Tbps packet processor device that enables performance and cost optimized solutions for a broad range of platforms in the Datacenter, Campus and Service provider environments. In addition, it has a full complement of L2/L3/L4 forwarding capabilities along with an advanced classification and policy engine. The Prestera CX devices are OpenFlow1.0 compliant and support additional extensions for a hybrid system design.

To shorten system manufacturers design cycles and accelerate time-to-market, Marvell provides complete development platforms and reference designs with, schematics, layout files and related documentation.



Marvell Prestera 98CX8297: 32 port QSFP Reference Design

**THE MARVELL ADVANTAGE:** Marvell chipsets come with complete reference designs which include board layout designs, software, manufacturing diagnostic tools, documentation, and other items to assist customers with product evaluation and production. Marvell's worldwide field application engineers collaborate closely with end customers to develop and deliver new leading-edge products for quick time-to-market. Marvell utilizes world-leading semiconductor foundry and packaging services to reliably deliver high-volume and low-cost total solutions.

**ABOUT MARVELL:** Marvell is a leader in storage, communications, and consumer silicon solutions. Marvell's diverse product portfolio includes switching, transceiver, communications controller, processor, wireless, power management, and storage solutions that power the entire communications infrastructure, including enterprise, metro, home, storage, and digital entertainment solutions. For more information, visit our Web site at [www.marvell.com](http://www.marvell.com).



Marvell Semiconductor, Inc.  
5488 Marvell Lane  
Santa Clara, CA 95054  
Phone 408.222.2500  
[www.marvell.com](http://www.marvell.com)

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