

# Marvell® 88Q5072 Secure Multi-Gig Automotive Switch with Integrated 100BASE-T1 PHY

11-port Automotive Ethernet Switch with Advanced Security & Support for TSN

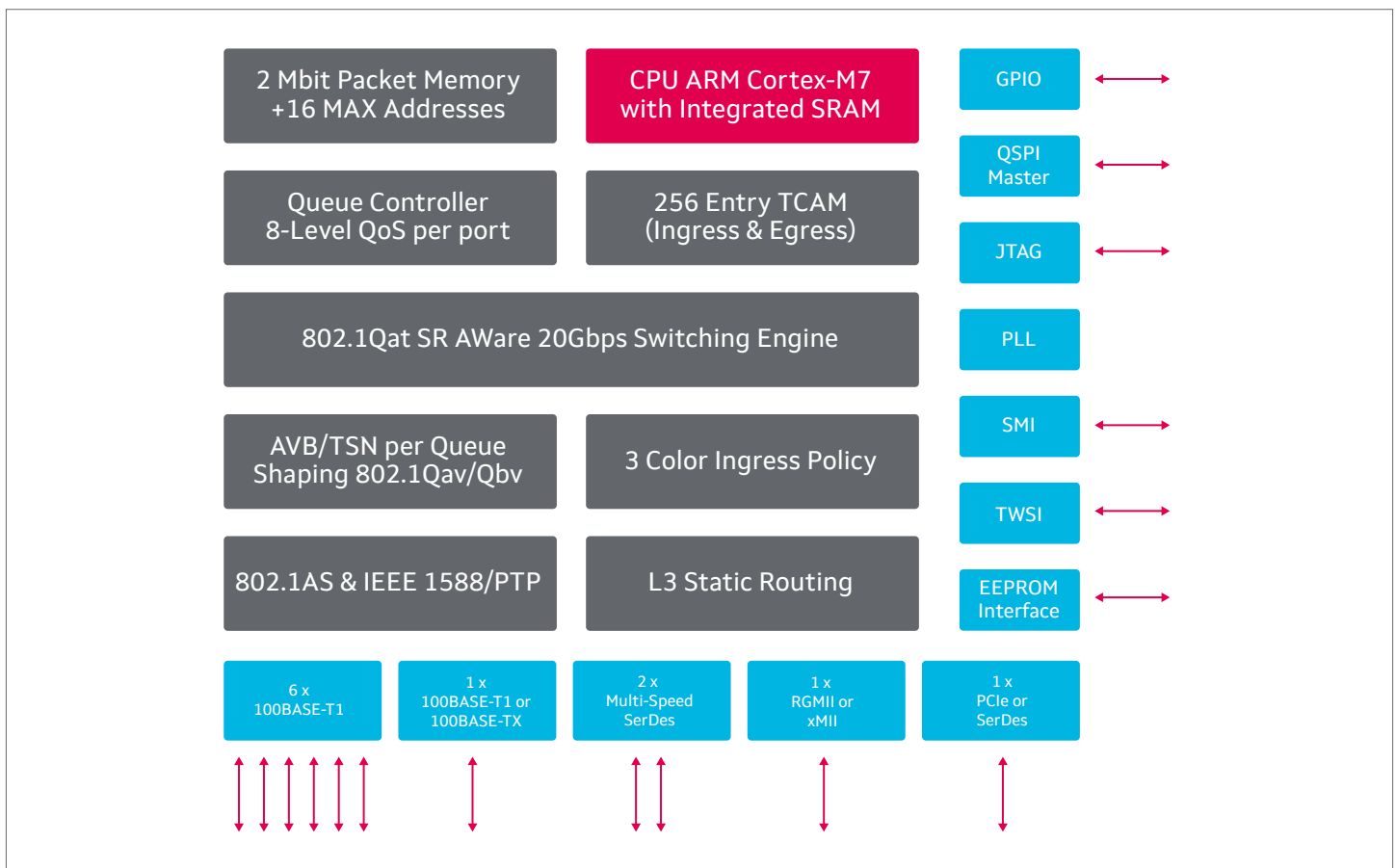
## Overview

Marvell's first generation high-port count Automotive Ethernet switch, 88Q5072, is an 11-port Ethernet gigabit capacity switch that is fully compliant with IEEE802.3 automotive standard delivering high performance with low power consumption.

The 11-port Ethernet switch offers 7 integrated IEEE 100BASE-T1 PHYs, of which 6 are fixed as 100BASE-T1 ports, and 7th port is configurable as 1x IEEE 100BASE-TX PHY, 2x Multi-Speed SerDes (1/2.5/5 Gbps) or SGMII port, 1x RGMII or MII/RMII/GMII, 1xPCIe or SerDes. The switch offers local and remote management capabilities, providing easy access and configuration of the device.

This switch includes a high-performance ARM® Cortex M7 CPU with dedicated on-chip memory to support AVB protocols such as Precision Time Protocol (PTP) and security firewall to protect from external malicious attacks. The switch employs advanced routing engine to support Gigabit routing of the incoming packets without the CPU intervention. It offers highest level of security with deep packet inspection (DPI) techniques, Denial of Service (DoS) engine and Trusted Boot functionality to secure the vehicle networks.

## Block Diagram



## Key Features

Features	Benefits
Processor	<ul style="list-style-type: none"><li>• Integrated ARM Cortex-M7 CPU, 350MHz with 1MB SRAM</li></ul>
IO Interfaces	<ul style="list-style-type: none"><li>• 6x IEEE 100BASE-T1</li><li>• 1x IEEE 100BASE-T1 or 100BASE-TX</li><li>• 1x Multi-Speed SerDes (1/2.5/5Gbps) or SGMII port</li><li>• 1x RGMII or MII/RMII/GMII ports</li><li>• 1x PCIe (Gen 3x1, 8Gbps) or SerDes(1/2.5Gbps)</li><li>• 2x SMI<ul style="list-style-type: none"><li>• Master interface to connect to external PHYs or additional switches</li><li>• Slave interface to manage the switch</li></ul></li><li>• Configurable GPIOs</li><li>• QSPI with configurable frequencies (19.2MHz-83.3MHz)</li><li>• TWSI Master interface</li><li>• JTAG Interface for debugging</li></ul>
Package Characteristics	<ul style="list-style-type: none"><li>• 17mmx17mm, 356-pin ball array TFBGA, 0.8 mm pitch</li></ul>
EEPROM	<ul style="list-style-type: none"><li>• Slave interface with loader to configure the switch (32Kb-512kb)</li></ul>

## Target Applications

- Central Gateway
- In-Vehicle Infotainment
- Advanced Driver Assistance Systems (ADAS)
- Body Domain Controller



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