



Security Advisory

Published: May 2026

CVE-2026-34880

Marvell was made aware of security vulnerabilities that could affect the Marvell 88SE9230 MagniPlus SATA controller. The controller includes a hardware mechanism that provides indirect access to the SPI NOR flash via PCIe configuration space. This access is enabled via vendor specific registers.

This SPI-access capability is part of the silicon design, is enabled by default, and cannot be disabled through firmware.

A host with sufficient privileges may manipulate these registers to read from or write to the SPI NOR flash, which could allow modification of the stored firmware image.

Marvell encourages customers to contact their Marvell representative for any additional support.