

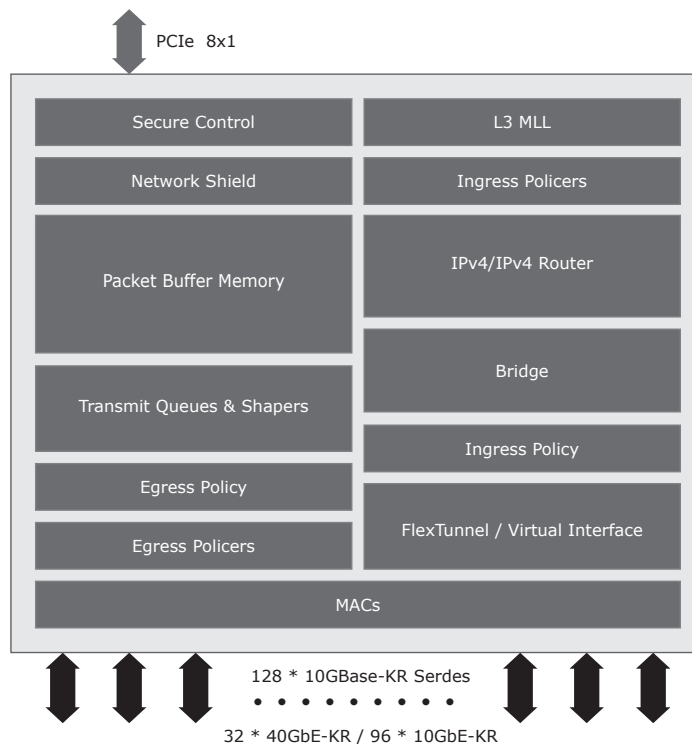
Marvell® Prestera® 98CX8297

Next Generation Switch Fabric for Modular Platforms

PRODUCT OVERVIEW

The Marvell® Prestera® CX family of packet processors enable high density 10GbE/40GbE solutions Datacenter Top of Rack (ToR), and Aggregation switches as well as Ethernet aggregation platforms for the Campus and Service Provider markets. The Prestera 98CX8297 provides line rate performance for up to 32 ports of 40 GbE or 96 ports of 10GbE with a full complement of Layer2/Layer3 switching, IPv4/v6 Unicast and Multicast routing capabilities along advanced classification and congesting management features.

BLOCK DIAGRAM



KEY FEATURES AND BENEFITS

SPECIAL FEATURES	BENEFITS
<ul style="list-style-type: none"> Integrated 10GBase-KR PHYs Datacenter Bridging 	<ul style="list-style-type: none"> Drive backplanes for modular platforms in ATCA, blade server applications Prioritization and Congestion management capabilities for next generation datacenter solution <ul style="list-style-type: none"> Priority Flow Control, Congestion Notification enable lossless mode of operation as well as burst isolation
<ul style="list-style-type: none"> Policy and classification engine CarrierSpan™ 	<ul style="list-style-type: none"> Built-in intelligence for application-level control Full complement of Metro Ethernet standards along with the latest service delivery techniques viz L2/L3 VPN, Q-in-Q, TR-101 etc <ul style="list-style-type: none"> Enables business and consumer networking services

▶ APPLICATIONS

The Prestera 98CX8297 is a 1.2Tbps packet processor device that enables performance and cost optimized solutions for a broad range of platforms in the Datacenter, Campus and Service provider environments. In addition, it has a full complement of L2/L3/L4 forwarding capabilities along with an advanced classification and policy engine. The Prestera CX devices are OpenFlow1.0 compliant and support additional extensions for a hybrid system design.

To shorten system manufacturers design cycles and accelerate time-to-market, Marvell provides complete development platforms and reference designs with, schematics, layout files and related documentation.



Marvell Prestera 98CX8297: 32 port QSFP Reference Design



To deliver the data infrastructure technology that connects the world, we're building solutions on the most powerful foundation: our partnerships with our customers. Trusted by the world's leading technology companies for 25 years, we move, store, process and secure the world's data with semiconductor solutions designed for our customers' current needs and future ambitions. Through a process of deep collaboration and transparency, we're ultimately changing the way tomorrow's enterprise, cloud, automotive, and carrier architectures transform—for the better.

Copyright © 2022 Marvell. All rights reserved. Marvell and the Marvell logo are trademarks of Marvell or its affiliates. Please visit www.marvell.com for a complete list of Marvell trademarks. Other names and brands may be claimed as the property of others.

Marvell_Prestera 98CX8297_PB Revised: 01/22