Marvell® Teralynx® 7
Data Center Ethernet Switch

The world’s fastest and most scalable switch family – 3.2 Tbps through 12.8 Tbps – featuring industry-leading analytics, low latency and programmability

Overview

Data centers are set to dominate IT infrastructure across the Cloud, Enterprises and Service Providers. Continued adoption of cloud, mobile, video, Anything-as-a-Service (XaaS), big data analytics, and machine learning is driving rapid growth of traffic inside data center networks and with end users. Architectural shifts such as micro-services based applications and the move to distributed storage using flash/NVMe over fabric are further increasing Ethernet traffic. In addition to an unlimited thirst for bandwidth, critical data center requirements include deeper, actionable analytics, programmability, high power efficiency, low latency and flexibility. These factors demand a fresh, innovative and focused approach to scalable, high performance networking silicon.

Marvell’s Teralynx Ethernet Switch Silicon family has been architected from the ground up to provide the most optimized Ethernet solutions for data centers. Marvell delivers the world’s highest performance switch silicon with large buffers, unmatched analytics through fine-grain telemetry, line-rate programmability, the industry’s best power efficiency in terms of performance per watt and very low latency. Marvell offers the broadest range of data center switches ranging from 3.2Tbps to 12.8Tbps, enabling compelling and unique systems such as 128 ports of 100GbE, 64 ports of 200GbE and 32 ports of 400GbE.

Block Diagram
### Key Features

<table>
<thead>
<tr>
<th>Features</th>
<th>Benefits</th>
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</thead>
<tbody>
<tr>
<td>- Industry leading performance and scale enables customers to deploy fewer network switches and tiers dramatically reducing cost, power, latency &amp; management</td>
<td>• Switch capacity up to 12.8Tbps with large buffers</td>
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<tr>
<td>- 50G SerDes lowers cost/bit and enables higher scale IO such as 200GbE &amp; 400GbE with backward compatibility</td>
<td>• Up to 256 SerDes that support 10G, 25G and 50G IO speed with proven, robust interoperability</td>
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<td>- Breakthrough visibility and analytics capabilities enable predictive, faster &amp; more accurate issue resolution, higher automation and self-healing autonomous networks</td>
<td>• Industry’s first switch to include high port count 200G and 400G connectivity using 50G PAM-4 with support for 10 / 25 / 40 / 50 / 100 / 200 and 400GbE connectivity</td>
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<tr>
<td>- Superior power efficient switch drives overall data center efficiency and better TCO</td>
<td>• Industry leading power efficiency, with 2X performance per watt over alternatives</td>
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<td>- Up to 128 ports of 100GbE, 64 ports of 200GbE, or 32 ports of 400GbE enables flatter &amp; cost-effective networks</td>
<td>• Comprehensive IP forwarding and highly scalable/flexible layer 2 and 3 tables for IPv4, IPv6 and hybrid networks</td>
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<td>- InnoFlex™ programmable forwarding pipeline enables support of custom &amp; new standard protocols without requiring ASIC spins to future proof the network</td>
<td>• Line-rate programmability to accommodate future networking protocols with software upgrades</td>
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<td>- Extensive tunneling capabilities such as IP-in-IP, GRE, MPLS, VXLAN and Geneve</td>
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<tr>
<td>- Very low latencies - cut-through and store-and-forward</td>
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<tr>
<td>- Advanced QoS/traffic management feature set such as DCB, RoCE and QCN</td>
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<tr>
<td>- FLASHLIGHT™ delivers breakthrough visibility and telemetry, including support for P4 In-band Network Telemetry (INT) along with critical extensions</td>
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### Customer Deployment Scenarios

- Data center networking infrastructure for the Cloud, Enterprises, HPC and Service Providers
- Fixed and modular (chassis) switches
  - Single chip fixed switches for ToR, Leaf & Spine
  - Multi-chip fixed switches for Leaf & Spine
  - Multi-chip modular switches for EoR, Leaf & Spine

### Software Support

- Common software development kit (SDK) across entire product line to help customers develop network operating software (NOS) stack easily
- High performance and highly resilient, modern software with a clean-sheet design
- Support for open APIs, including OCP SAI (Switch Abstraction Interface)
### 6-12X Advantage Over Alternatives
- 6X over 6.4Tbps switch chips
- 12X over 3.2Tbps switch chips
- Dramatically lowers cost, power and latency with simpler management
- Superior visibility and analytics

### Single Chip System Design Examples
- 1U: 32 x 400G (QSFP-DD or OSFP)
- 1U: 32 x 100/200G
- 2U: 64 x 100/200G (QSFP28/56)
- 4U: 128 x 100G (QSFP28)

### Multi Chip System Design Examples
- 2U: 64 x 400G (QSFP-DD or OSFP)
- Chassis: 128 x 400G (i.e. 512 x 100G ports)
- Chassis: 256 x 400G (i.e. 1024 x 100G ports)

### Ordering Information

<table>
<thead>
<tr>
<th>Part Number</th>
<th>Capacity (Tbps)</th>
<th># of SerDes @ Gbps</th>
<th>100G ports</th>
<th>200G ports</th>
</tr>
</thead>
<tbody>
<tr>
<td>IVM77700</td>
<td>12.8</td>
<td>256 @ 10/25/50</td>
<td>128</td>
<td>64</td>
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<tr>
<td>IVM77610</td>
<td>8</td>
<td>160 @ 10/25/50</td>
<td>80</td>
<td>40</td>
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<tr>
<td>IVM77500</td>
<td>6.4</td>
<td>256 @ 10/25</td>
<td>64</td>
<td>32</td>
</tr>
<tr>
<td>IVM77310</td>
<td>3.2</td>
<td>128 @ 10/25</td>
<td>32</td>
<td>16</td>
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