PRODUCT OVERVIEW

The Marvell® Alaska® 88E3015, 88E3016, and 88E3018 are Marvell’s fourth-generation DSP-based physical layer (PHY) transceivers for Fast Ethernet (FE) applications. The devices contain all the active circuitry required to convert data streams to and from a Media Access Controller (MAC) and to and from the physical media. The 88E3015, 88E3016, and 88E3018 devices incorporate IEEE 802.3u auto-negotiation, supporting both 100BASE-TX and 10BASE-T networks over twisted-pair cable in full-duplex or half-duplex mode.

The 88E3015, 88E3016, and 88E3018 all support the Reduced Gigabit Media Independent Interface (RGMII), with the 88E3015 and 88E3018 also supporting the Media Independent Interface (MII). They all feature a mode of operation supporting IEEE-compliant 100BASE-FX fiber-optic networks. Additionally, the devices implement Far-End-Fault-Indication (FEFI) in order to provide a mechanism for transferring information from the local station to the link partner that indicates that a remote fault has occurred in 100BASE-FX mode.

The Alaska 88E3015, 88E3016, and 88E3018 feature the Marvell Virtual Cable Tester® (VCT™) technology, which enables IT managers and networking equipment manufacturers to remotely analyze the quality and characteristics of the attached cable plant. Additionally, the devices use advanced mixed-signal processing and power-management techniques for extremely low power dissipation and high port count system integration.

The 88E3015, 88E3016, and 88E3018 devices can operate from a single 2.5v or 3.3v supply. They are offered in low-pin count QFN packages which reduce board space and are ideal for small form-factor applications. In addition, the 88E3016 and 88E3018 are the only single-port FE PHYs available in the industry which are pin-upgradeable to Gigabit Ethernet.

BLOCK DIAGRAM

[Diagram of the 88E3018 Functional Block Diagram]

Fig 1. 88E3018 Functional Block Diagram
**FEATURES**

- Supports RGMII interface (MII also supported in the 88E3015 and 88E3018)
- Automatic MDI/MDIX crossover at all speeds of operation
- PECL interface supporting 100BASE-FX applications
- IEEE 802.3u compliant auto-negotiation
- MDC/MDIO management interface
- Virtual Cable Tester (VCT)
- Coma mode support and energy detect feature
- Supports 802.3ah unidirectional enable
- Supports three LEDs per port
- 88E3016/88E3018: Pin-upgradeable to Marvell Gigabit Ethernet PHY
- 88E3016/88E3018: Supports IEEE 1149.1 JTAG

**BENEFITS**

- Reduces pin count and offers Gigabit Ethernet upgrade
- Eases installation and reduces costs by working with both straight and cross-over cables
- Supports FE over fiber applications
- Automatically configures to 10 or 100 Mbps
- Provides flexible management options
- Identifies and isolates cable faults
- Provides flexible power-management functionality
- Enables the PHY transmit path even if no link is established
- Allows for user configuration of programmable LEDs
- Provides for future upgrade to Gigabit Ethernet with increased design flexibility
- Simplifies board level testing/debugging

**APPLICATIONS**

Despite the existence of advanced Gigabit Ethernet, 10/100 Ethernet maintains a strong presence in corporate networks, in small home networks and in various media devices. The Marvell Alaska single-port FE PHY transceivers provide connectivity solutions for a variety of applications including DVRs, game consoles, media vaults, video cameras, printers, and notebook or desktop PCs. The Alaska 88E3015, 88E3016, and 88E3018 offer low-power dissipation, enabling system manufacturers to decrease their system costs by reducing power supply requirements.

**FEATURES BENEFITS**

<table>
<thead>
<tr>
<th>Features</th>
<th>88E3015</th>
<th>88E3016</th>
<th>88E3018</th>
</tr>
</thead>
<tbody>
<tr>
<td>Pin-Upgradeable to GbE</td>
<td>No</td>
<td>Yes</td>
<td>Yes</td>
</tr>
<tr>
<td>MII</td>
<td>Yes</td>
<td>No</td>
<td>Yes</td>
</tr>
<tr>
<td>RGMII</td>
<td>Yes</td>
<td>Yes</td>
<td>Yes</td>
</tr>
<tr>
<td>Virtual Cable Tester®</td>
<td>Yes</td>
<td>Yes</td>
<td>Yes</td>
</tr>
<tr>
<td>Fiber Support</td>
<td>Yes</td>
<td>Yes</td>
<td>Yes</td>
</tr>
<tr>
<td>Parallel LEDs</td>
<td>Yes</td>
<td>Yes</td>
<td>Yes</td>
</tr>
<tr>
<td>Power Management</td>
<td>Yes</td>
<td>Yes</td>
<td>Yes</td>
</tr>
<tr>
<td>JTAG Support</td>
<td>No</td>
<td>Yes</td>
<td>Yes</td>
</tr>
</tbody>
</table>

**Fig 2. Device Feature Comparison**